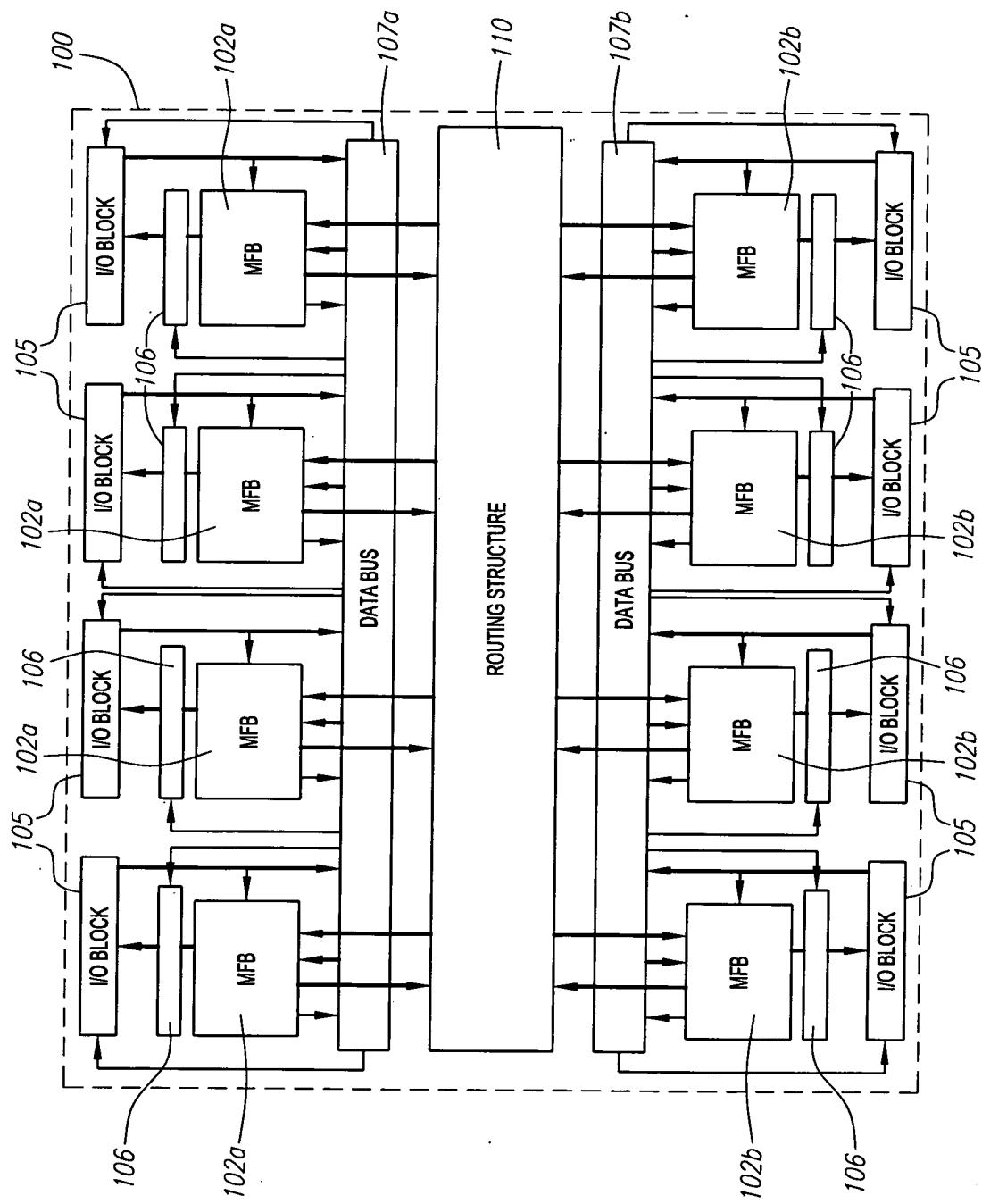


PROGRAMMABLE LOGIC DEVICE WITH ENHANCED
WIDE INPUT PRODUCT TERM CASCADING

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FIG. 1



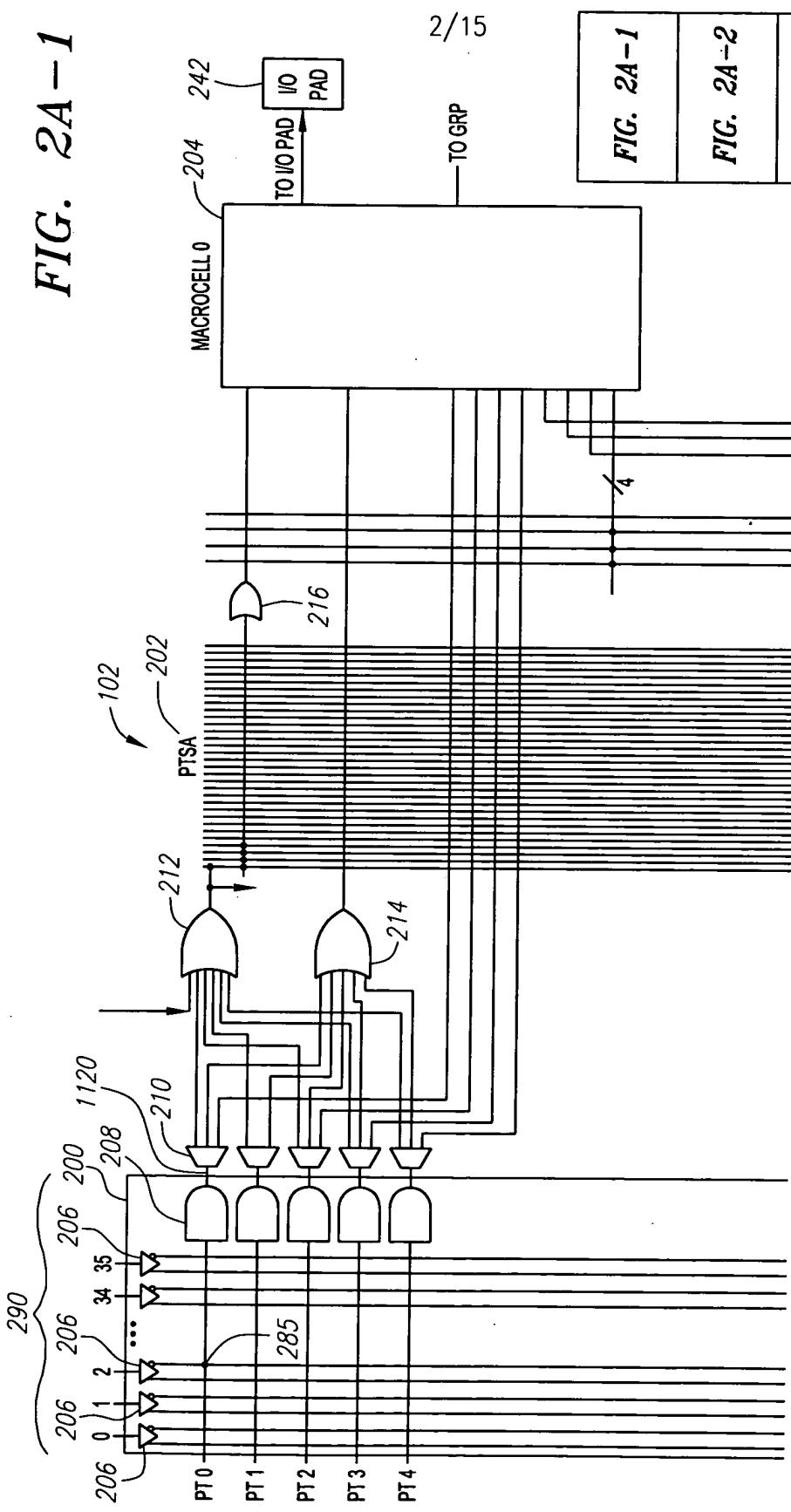
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FIG. 2A-1



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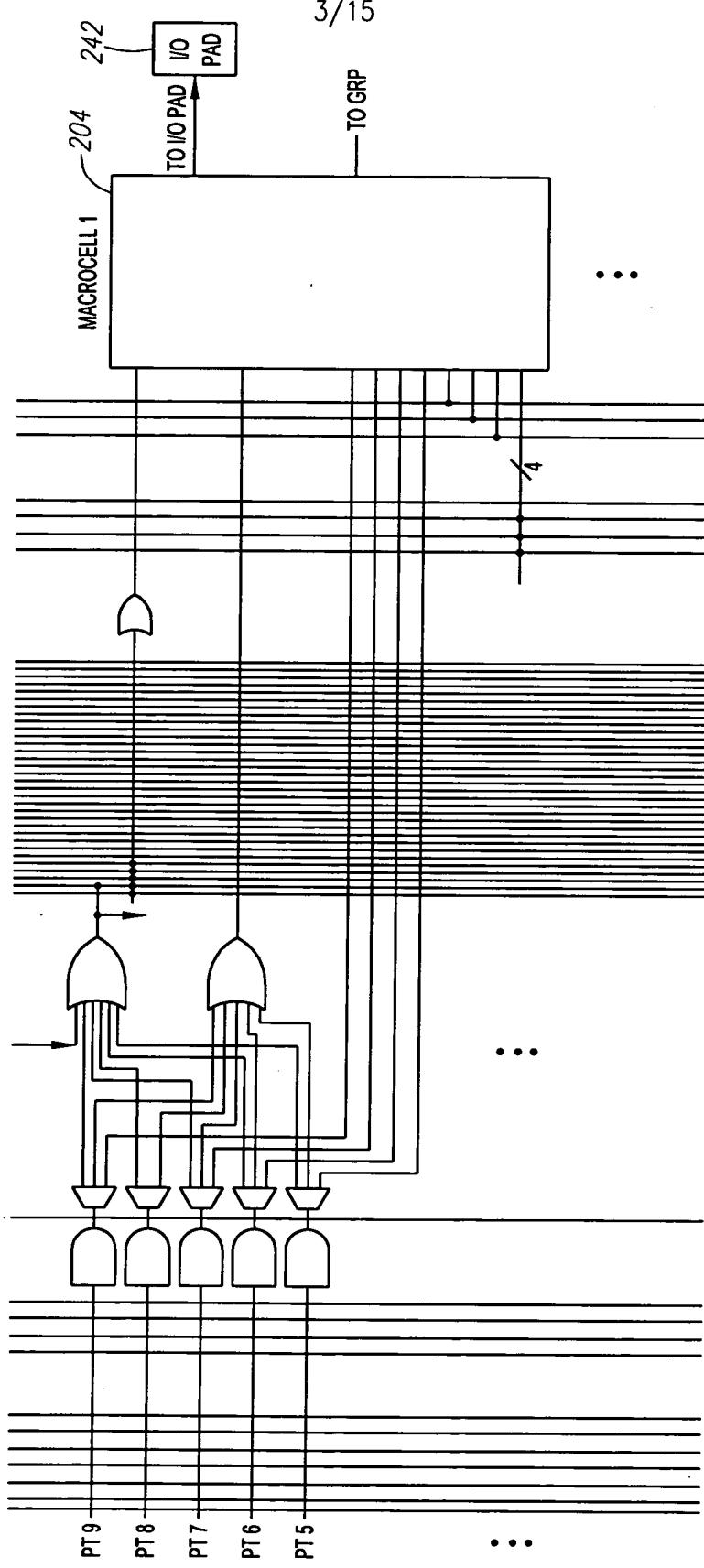


FIG. 2A-2

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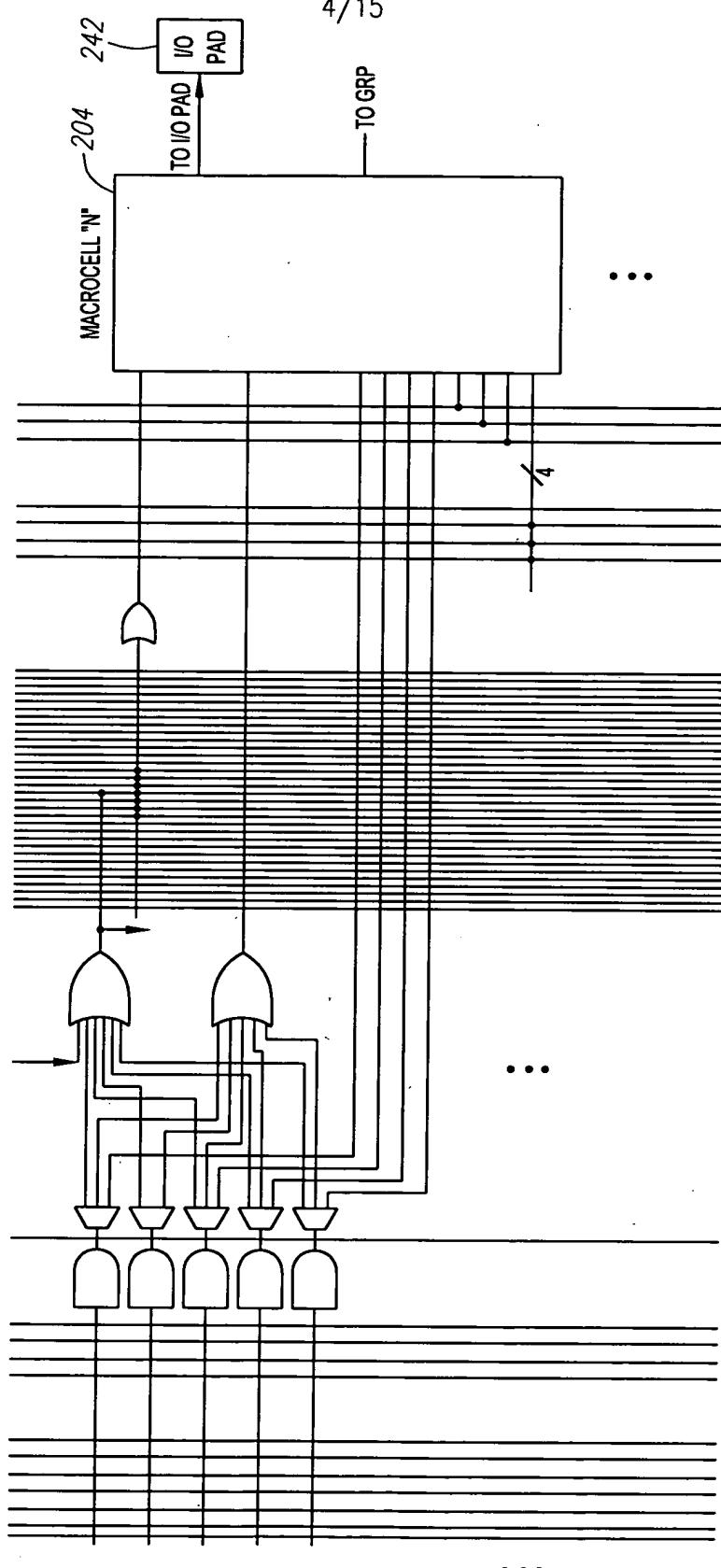


FIG. 2A-3

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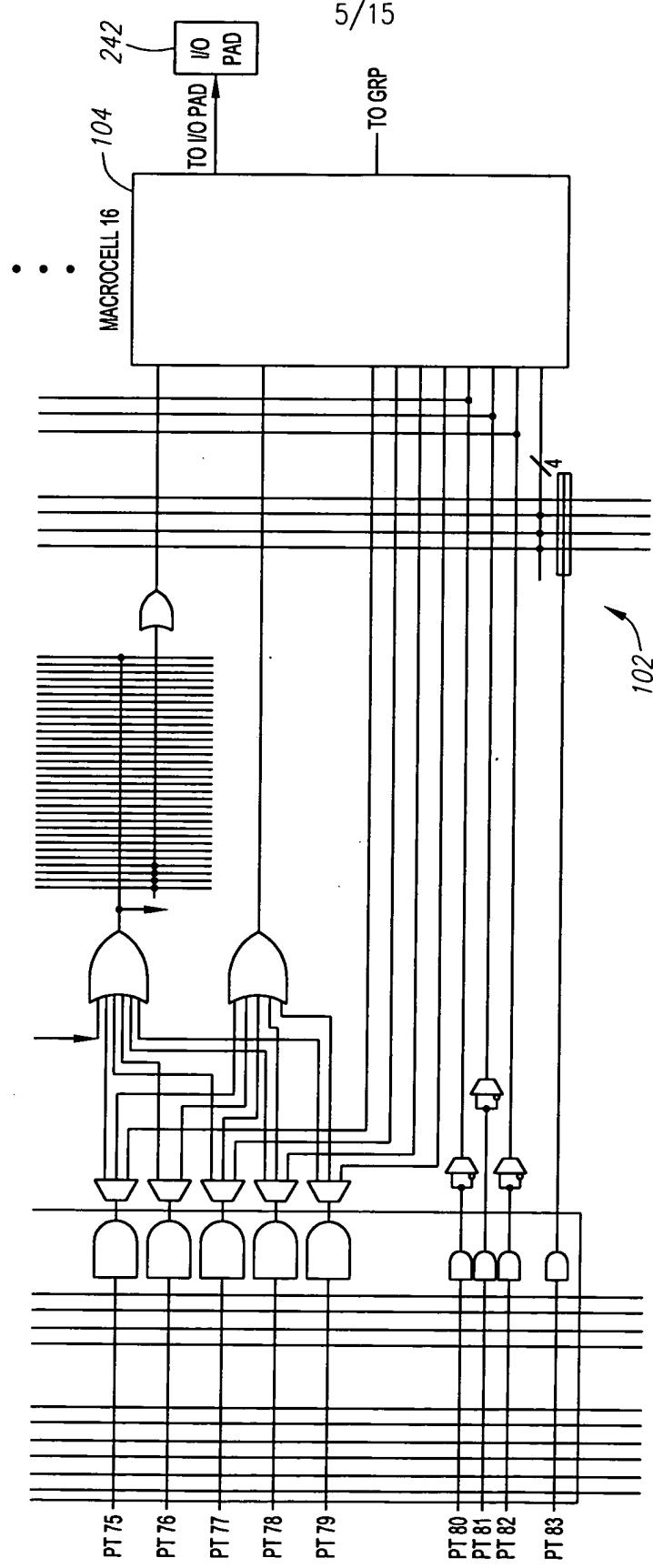


FIG. 2A-4

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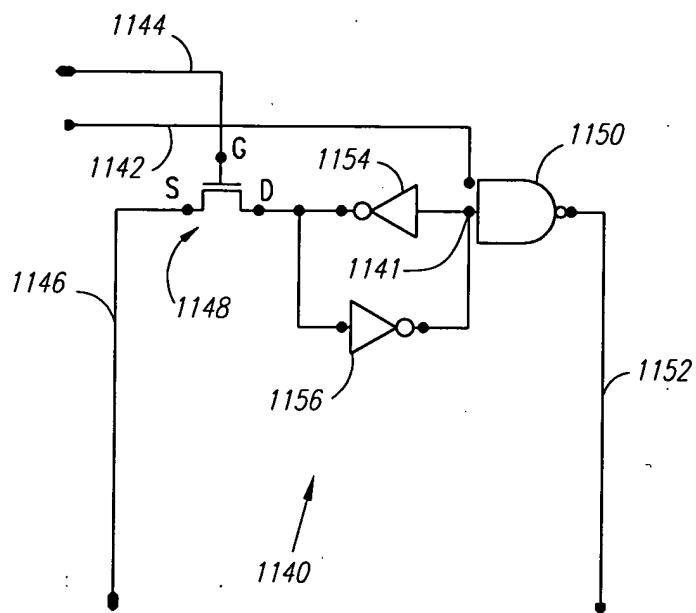


FIG. 3

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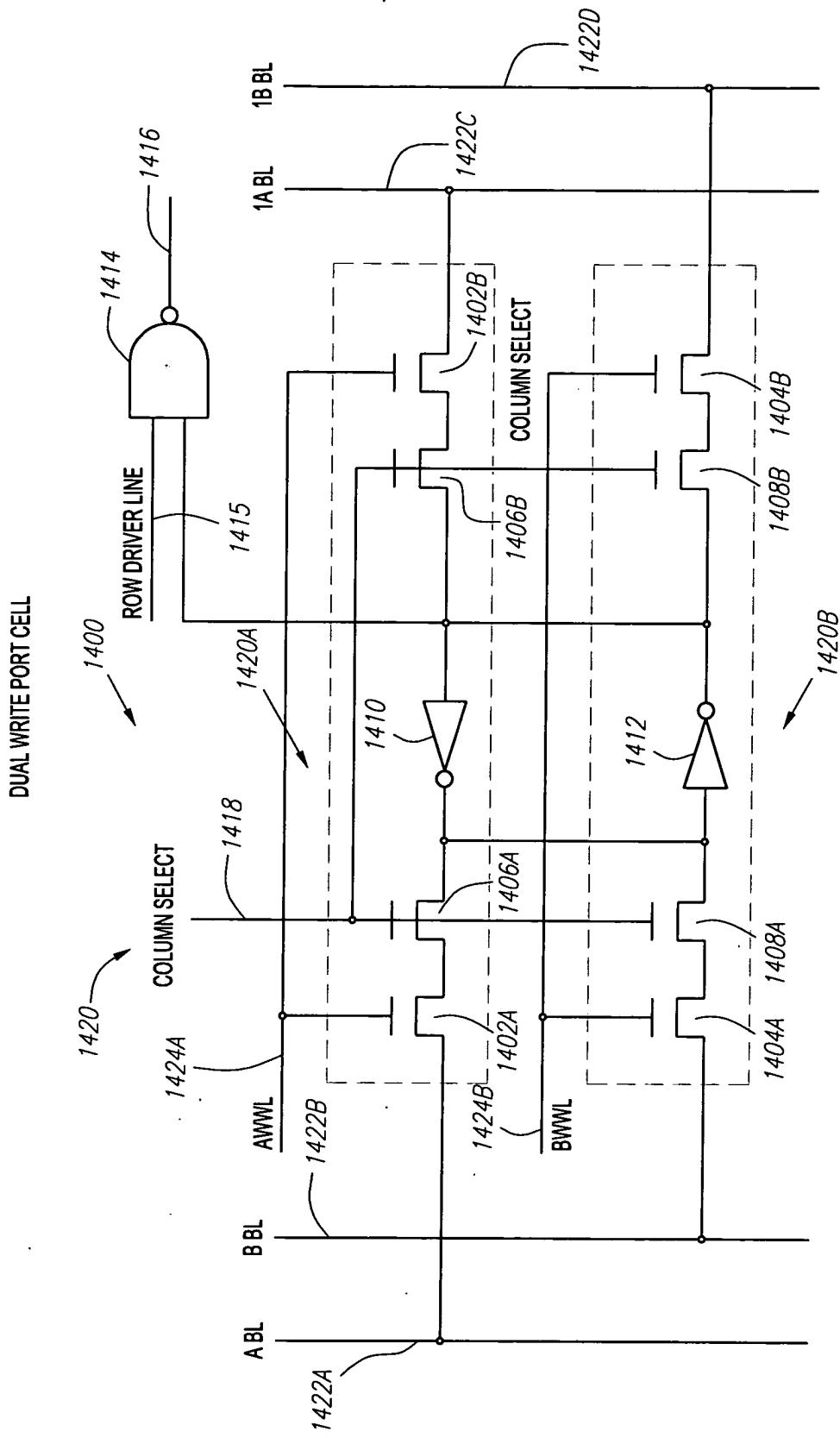


FIG. 4

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED

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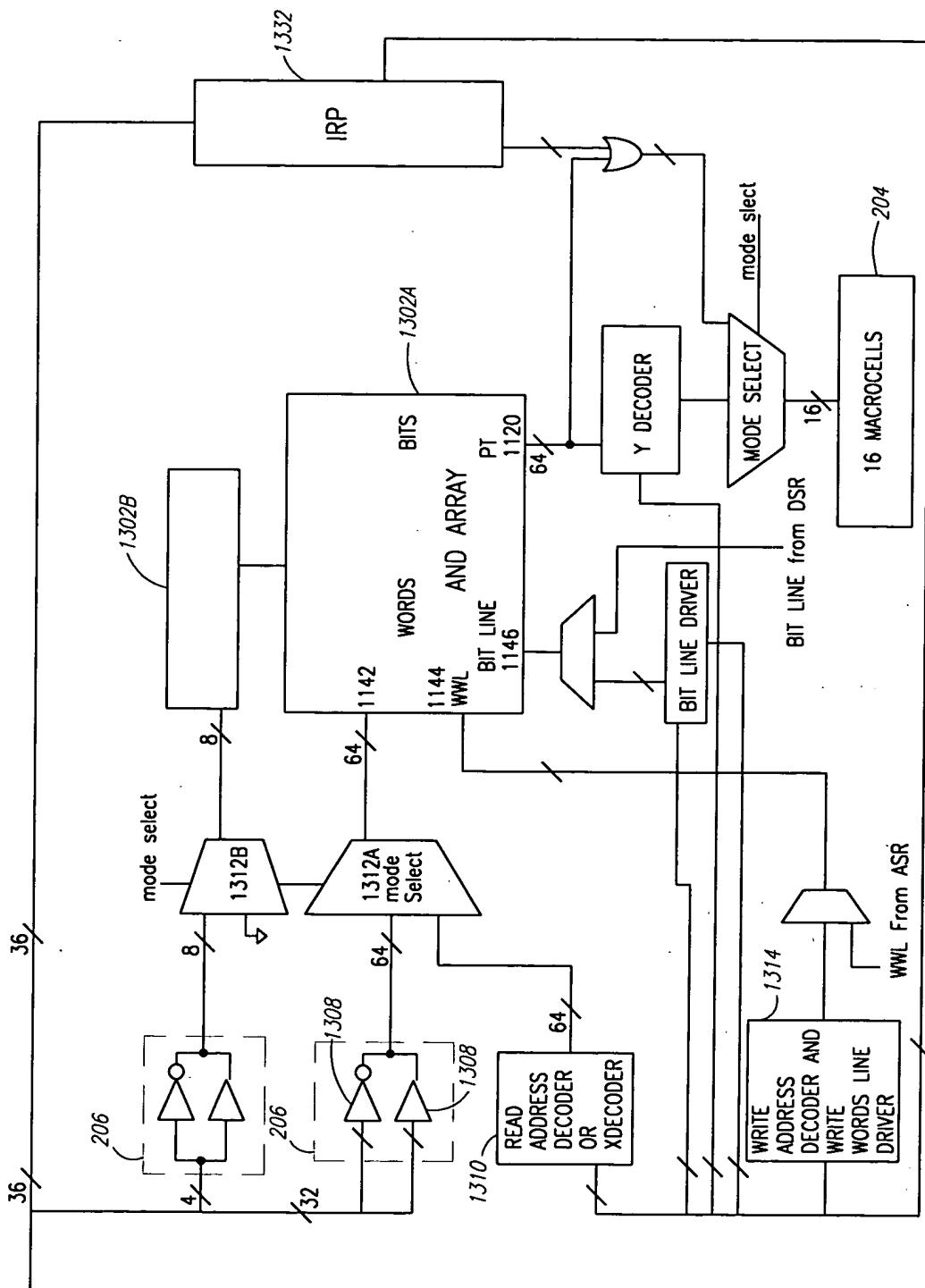


FIG. 5A

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED
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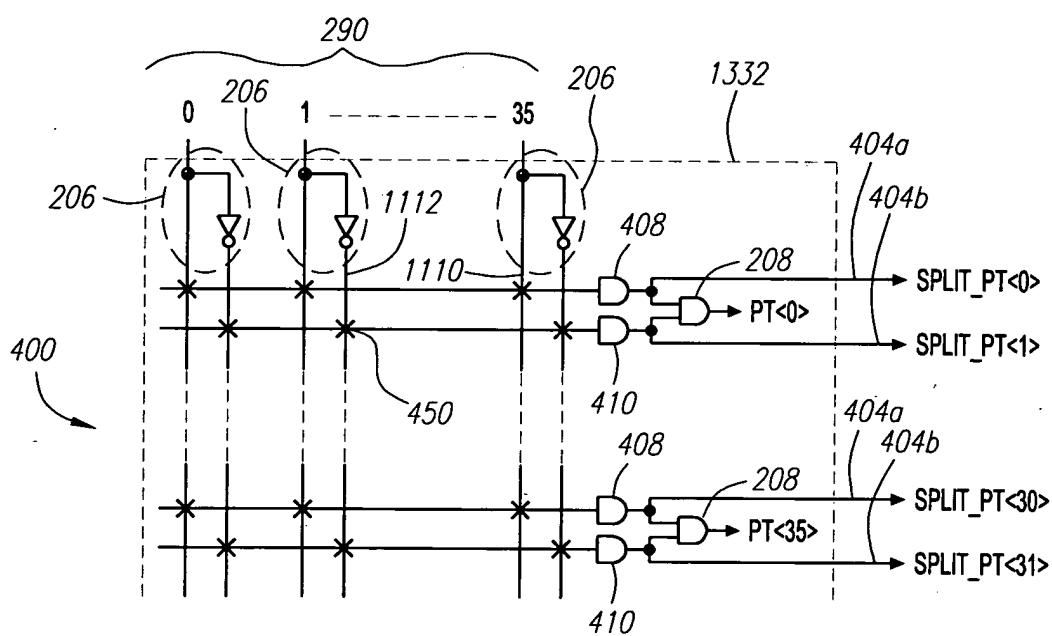


FIG. 5B

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED
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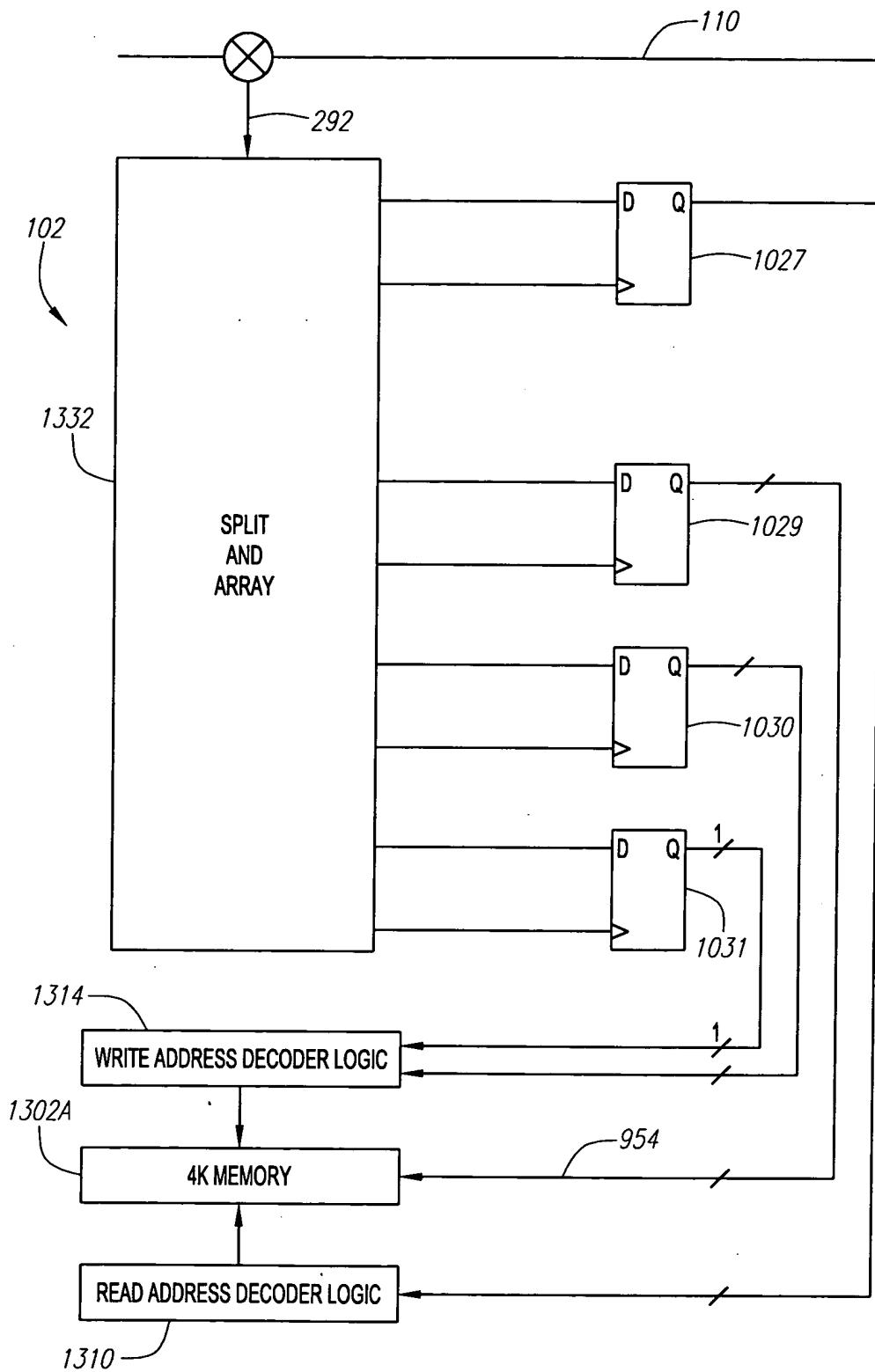


FIG. 6

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED
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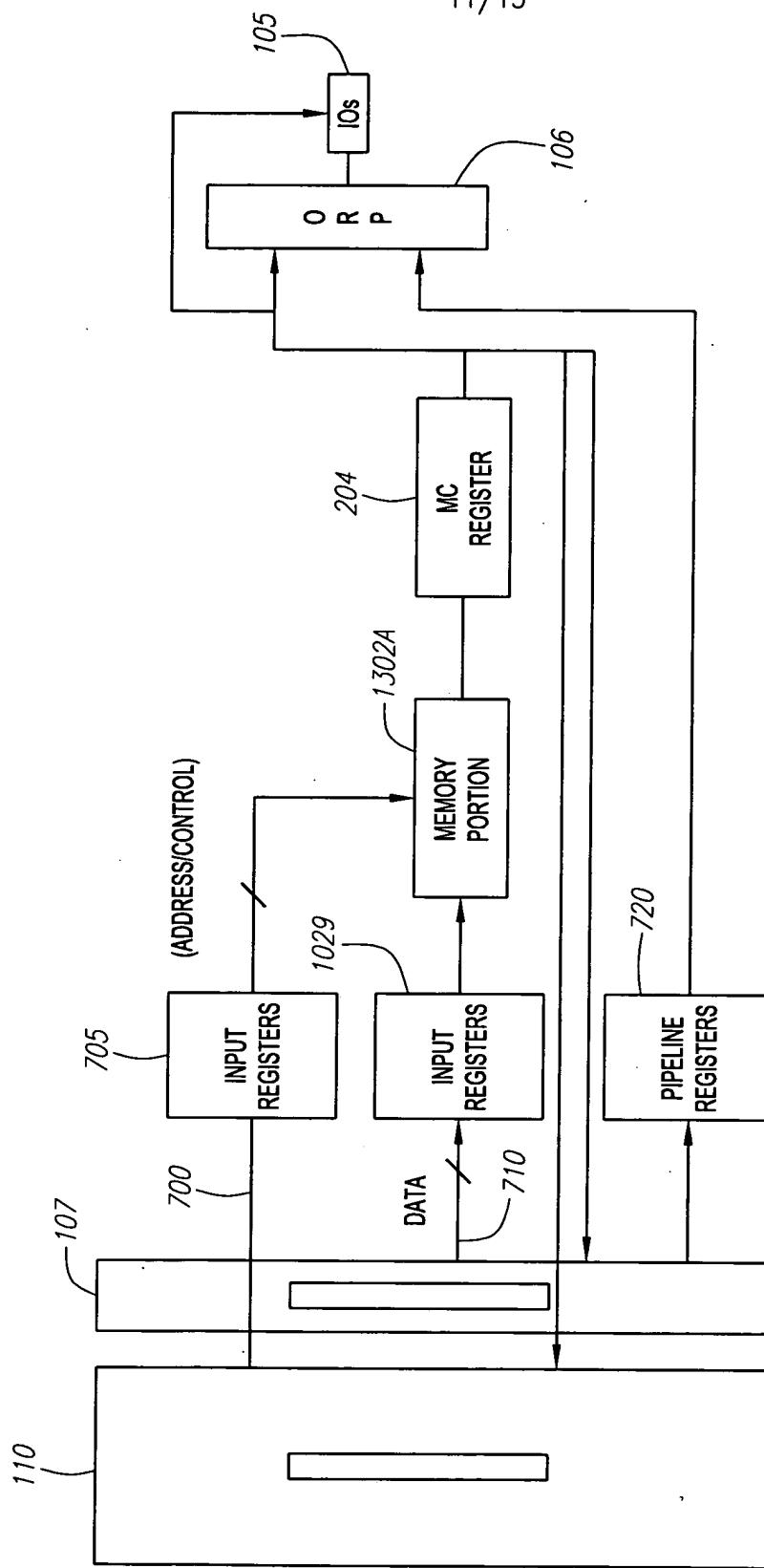


FIG. 7

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED

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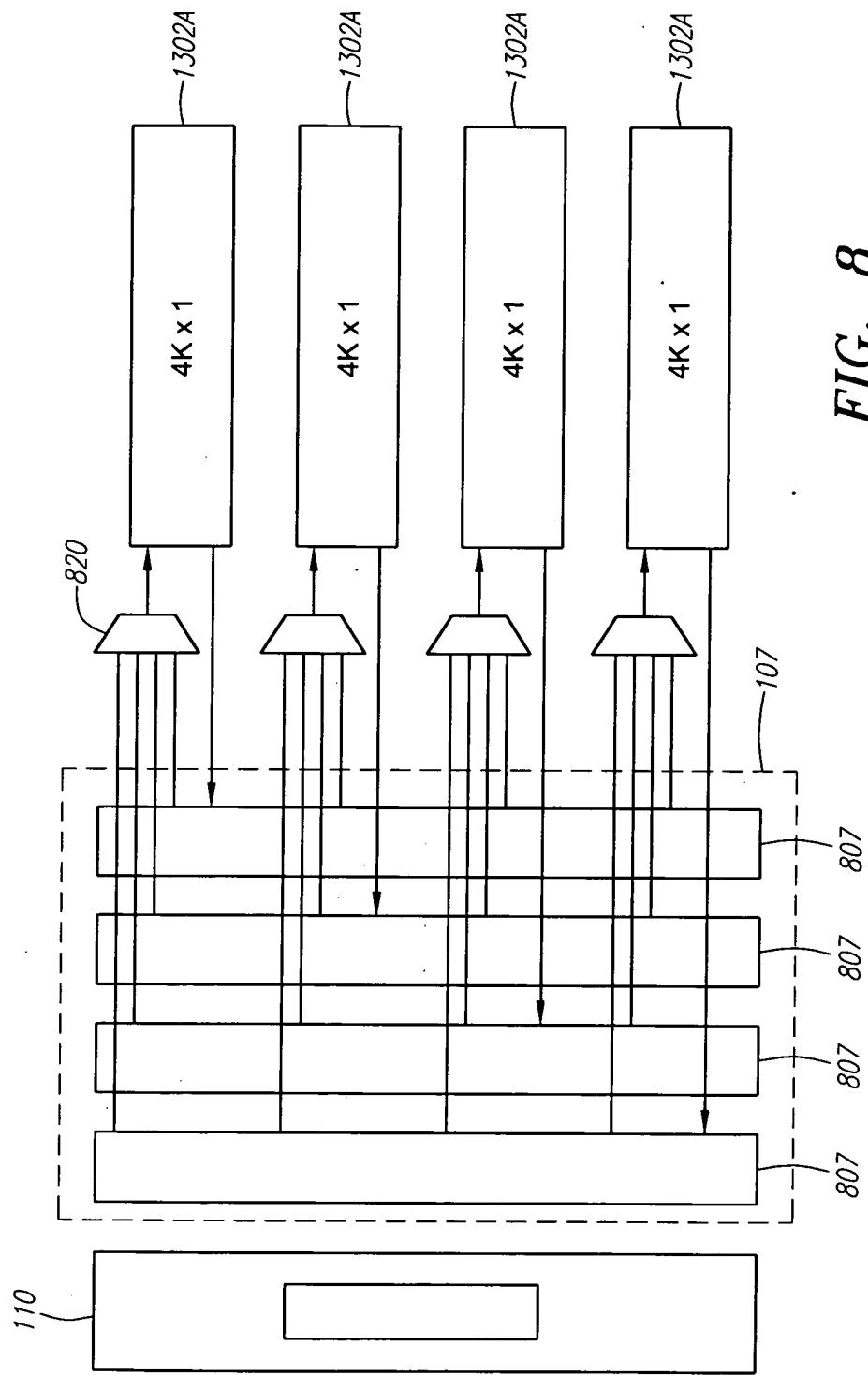


FIG. 8

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED WIDE INPUT PRODUCT TERM CASCADING

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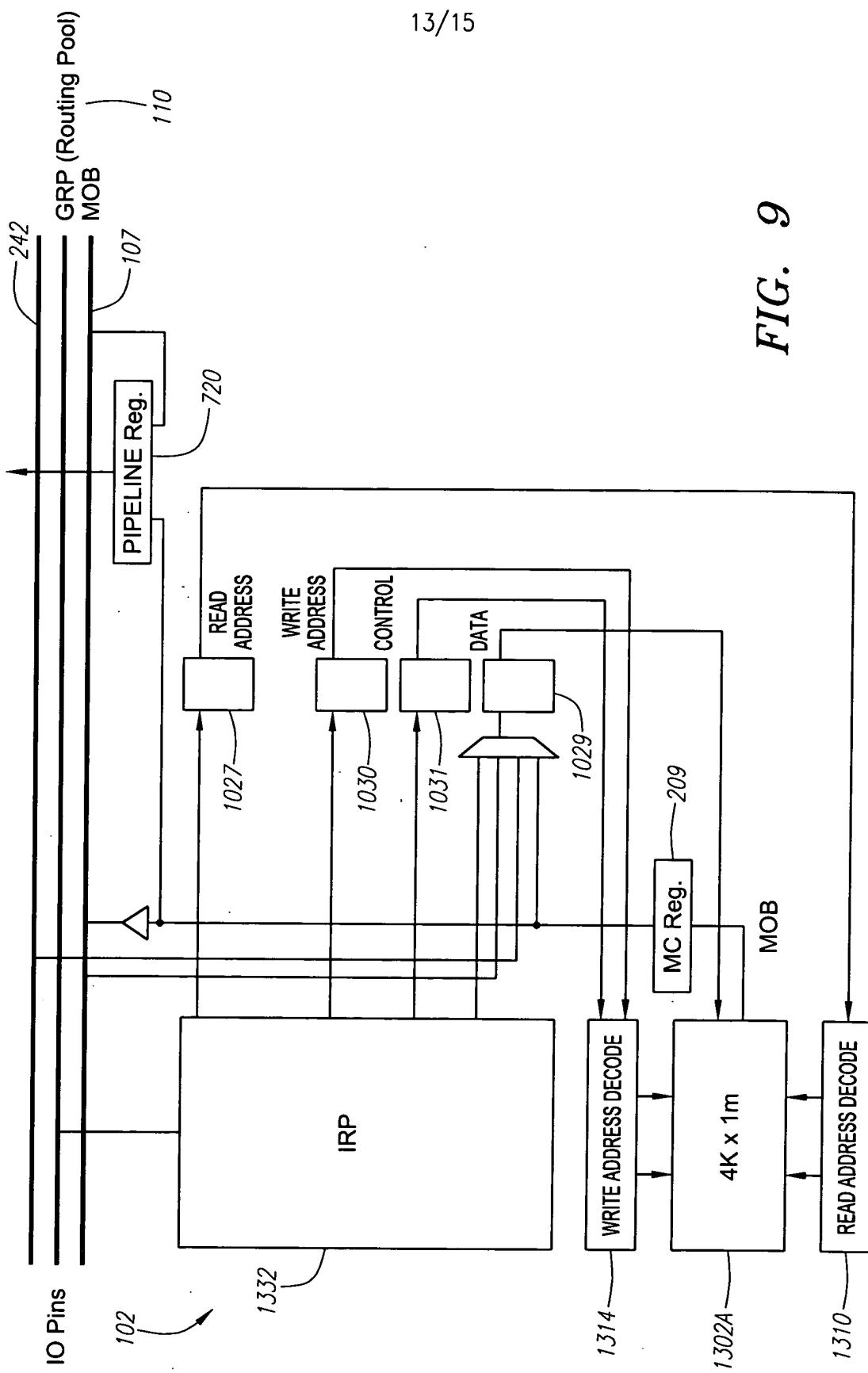


FIG. 9

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED
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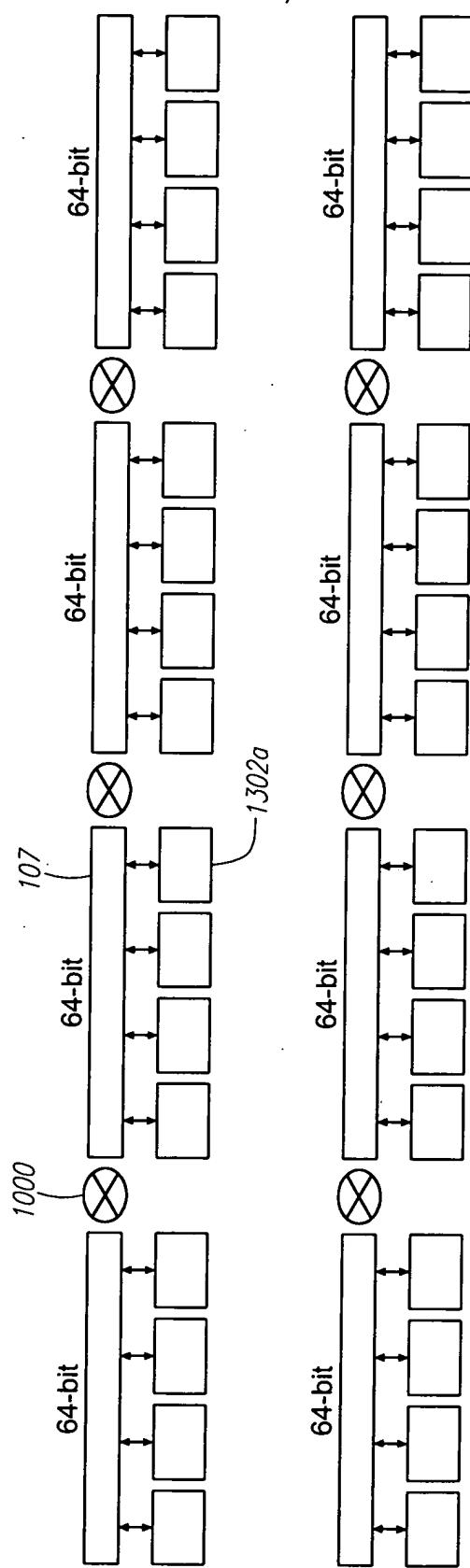


FIG. 10

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED
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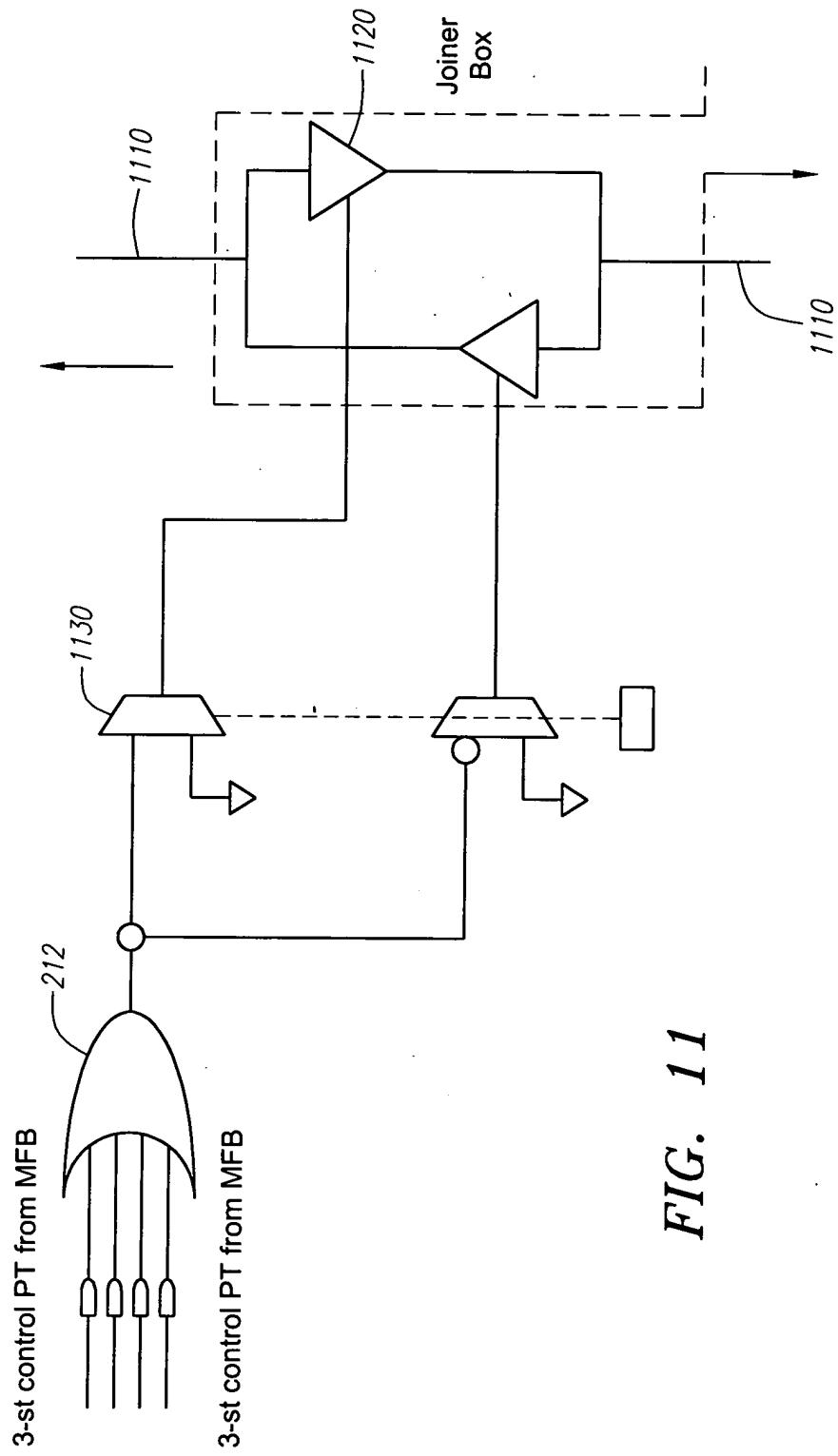


FIG. 11